

# A Simulated Annealing Algorithm for the Shortest Loop Design Problem in a Block Layout

R. Bouabda, B. Jarboui, H. Kammoun

This paper addresses the problem of finding a shortest loop covering at least one edge of each cell of a fixed block layout. This problem is known to be NP-hard. Hence, simulated annealing algorithm was developed to tackle large size problems. The performance of the proposed algorithm is tested by numerical experiments on a large number of representative problems.