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# EXACT SCALAR PRODUCT ON HIGH-PERFORMANCE COMPUTING* 

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#### Abstract

Algorithms for summation and dot product of floating point numbers are presented which are fast in terms of measured computing time. The algorithms are widely applicable because they require only addition, subtraction and multiplication of floating point numbers in the same working precision as the given data, no other data format (higher precision) is necessary. The computed results are shown to be as accurate as if computed in doubled or $k$-fold working precision.


1. Introduction. In this paper fast algorithms presented to compute the dot product of floating point numbers in a specified precision. This algorithms are based on so called error-free transformations [10]. It is for long known that the approximation error of a floating point operation is itself a floating point number. That means, a pair of floating point numbers can be transformed into another pair of floating point numbers, one being equal to the result of the floating point operation between the input numbers and the other representing the (precise) approximation error. Fortunately, very fast algorithms are known to compute such pairs for all operations we need, that is for addition, subtraction and multiplication. Amazingly, those algorithms consist only of the same basic floating point operations [2], [7], [9]. Throughout the paper assume a floating point arithmetic adhering to IEEE 754 floating point standard [5], and that no overflow occurs, but we allow underflow. We will use only one working precision for floating point computations. If this working precision is IEEE 754 double precision, then this corresponds to 53 bits precision including implicit one. The set of floating point numbers in this working precision is denoted by $\mathbb{F}$, the relative rounding error unit by eps, and the underflow unit - by eta. For IEEE 754 double it is eps $=2^{-53}$ and eta $=2^{-1073}$. We denote by $\mathrm{fl}($.$) the result of a floating point computation, where all$ operations inside parentheses are executed (in floating point) in working precision. It is well known that for each of the four basic operations the approximation error of the floating point operation can be expressed by a floating point number:

$$
\begin{array}{rll}
x=f l(a \pm b) \Rightarrow a \pm b=x+y & \text { for } & y \in \mathbb{F}, \\
x=f l(a \cdot b) \Rightarrow a \cdot b=x+y & \text { for } & y \in \mathbb{F},  \tag{1}\\
x=f l(a / b) \Rightarrow a=x \cdot b+y & \text { for } & y \in \mathbb{F},
\end{array}
$$

[^0]where in the case of multiplication and division it is assumed that no underflow occurs. This is true for all floating point numbers $a, b \in \mathbb{F}$. These are error-free transformations of the pair $(a, b)$ into $(x, y)$, where $x$ is the result of the corresponding floating point operation. Note that no information is lost; the equalities in (1) are mathematical identities.
2. Error-free transformations. Our goal is to extend the error-free transformations for the sum and product of two floating point numbers to sums of vector elements and to dot products, respectively. Therefore, the first two of the identities in (1) will play a fundamental role in the following. Fortunately, the quantities $x, y$ are effectively computable, without any if-statements, only using ordinary floating point addition, subtraction and multiplication. For addition ( and subtraction by adding $-b$ ) the following algorithm by Knuth [6] can be used.
\[

$$
\begin{gathered}
\text { function }[x, y]=\operatorname{TwoSum}(a, b) \\
x=f l(a+b) \\
z=f l(x-a) \\
y=f l((a-(x-z)+(b-z))
\end{gathered}
$$
\]

ALGORITHM(1). Error-free transformation of the sum of two floating point numbers.

The multiplication routine needs to split the input arguments into two parts, respectively. The number $p$ is given by eps $=2^{-p}$, and we define $s:=\lceil p / 2\rceil$; in IEEE 754 double precision it is $p=53$ and $s=27$. The following algorithm by Dekker [3] splits a floating point number $a \in \mathbb{F}$ into two parts $x, y$, where both parts have at most $s-1$ nonzero bits. In a practical implementation the variable "factor" can, of course, be replaced by a constant.

$$
\begin{gathered}
\text { function }[x, y]=\operatorname{Split}(a) \\
\text { factor }=2^{s}+1 \\
c=f l(\text { factor } . a) \\
x=f l(c-(c-a)) \\
y=f l(a-x)
\end{gathered}
$$

ALGORITHM(2): Error-free split of a floating point number into two parts.
The multiplication to calculate " $c$ " in Algorithm 2 cannot cause underflow except when the input " $a$ " is deep in the gradual underflow range. Since addition and subtraction is exact in case of underflow, the analysis [3] of Split is still valid and we obtain

$$
a=x+y \quad \text { and } \quad x \quad \text { and } \quad y \text { non-overlapping with } \quad|y| \leq|x|
$$

With this the following multiplication routine by G. W. Veltkamp (see [3]) can be formulated.

$$
\begin{gathered}
\text { function }[x, y]=\operatorname{TwoProduct}(a, b) \\
x=\mathrm{fl}(a \cdot b) \\
{\left[a_{1}, a_{2}\right]=\operatorname{Split}(a)} \\
{\left[b_{1}, b_{2}\right]=\operatorname{Split}(b)} \\
y=f l\left(a_{2} \cdot b_{2}-\left(\left(\left(x-a_{1} \cdot b_{1}\right)-a_{2} \cdot b_{1}\right)-a_{1} \cdot b_{2}\right)\right)
\end{gathered}
$$

ALGORITHM(3): Error-free transformation of the product of two floating point numbers.
Note that no branches, only basic and good optimizable floating point operations are necessary. In case no underflow occurs, we know [3] that

$$
a \cdot b=x+y \quad \text { and } \quad x=f l(a \cdot b)
$$

We summarize the properties of the algorithms TwoSum and twoProduct as follows (flops denotes the number of floating point operations counting additions, subtractions and multiplications separately).

Theorem 1. Let $a, b \in \mathbb{F}$ and denote the results of Algorithm 1 (TwoSum) by $x, y$. Then, also in the presence of underflow,

$$
a+b=x+y, x=f l(a+b),|y| \leq e p s|x|,|y| \leq e p s|a+b| .
$$

The algorithm TwoSum requires 6 flops.
Let $a, b \in \mathbb{F}$ and denote the results of Algorithm 3 (TwoProduct) by $x$, $y$. Then, if no underflow occurs,

$$
a \cdot b=x+y, x=f l(a \cdot b),|y| \leq e p s|x|,|y| \leq e p s|a \cdot b|
$$

and in the presence of underflow,
$a \cdot b=x+y+5 \eta, x=f l(a \cdot b),|y| \leq e p s|x|+5$ eta $a,|y| \leq e p s|a \cdot b|+5$ eta with $|\eta| \leq$ eta. The algorithm TwoProduct requires 17 flops.
3. Summation. Let floating point numbers $p_{i} \in \mathbb{F}, 0 \leq i \leq n$, be given. In this section we aim to compute a good approximation of the sum $s=\sum p_{i}$. With Algorithm 1 we have a possibility to add two floating point numbers with exact error term. So we may try to cascade Algorithm 1 and sum up the error in order to improve on the result of the ordinary floating point summation $f l\left(\sum p_{i}\right)$.

$$
\begin{gathered}
\text { function res }=\operatorname{Sum} K(p, K) \\
\text { for } k=1: K \\
p=\operatorname{Vec} \operatorname{Sum}(p) \\
\text { res }=\mathrm{fl}\left(\left(\sum_{i=0}^{n-2} p_{i}\right)+p_{n-1}\right)
\end{gathered}
$$

ALGORITHM(4): $K$-fold error-free vector transformation for summation and approximate sum.
Where $\operatorname{VecSum}(\mathrm{p})$ is transforms Algorithm for the vector $p$ without changing the sum and replace $p_{n}$ by $\mathrm{f}\left(\sum p_{i}\right)$.

$$
\begin{gathered}
\text { function } p=\operatorname{VecSum}(\mathrm{p}) \\
\text { for } i=1: n \\
{\left[p_{i}, p_{i-1}\right]=\operatorname{TwoSum}\left(p_{i}, p_{i-1}\right)}
\end{gathered}
$$

ALGORITHM(5): Error-free vector transformation for summation.

Proposition 1. Let floating point numbers $p_{i} \in \mathbb{F}, 0 \leq i \leq n$, be given and $K \geq 1$. Then, also in the presence of underflow, the result "res" of Algorithm 4 (SumK) satisfies

$$
\begin{aligned}
|r e s-s| & \leq\left(e p s+\gamma_{n-1} \gamma_{4 n-4}\right)|s|+\gamma_{2 n-2}^{K+1} S \\
& \leq\left(e p s+\gamma_{2 n}^{2}\right)|s|+\gamma_{2 n}^{K+1} S
\end{aligned}
$$

where $s:=\sum p_{i}$ and $S:=\sum\left|p_{i}\right|$. Algorithm 4 requires $(6 K+1)(n-1)$ flops.
4. Dot product. With Algorithm 3 we already have an error-free transformation of the product of two floating point numbers into the sum of two floating point numbers. The algorithm for $K=1$ corresponding to doubled working precision is as follows.

$$
\begin{gathered}
\text { function res }=\operatorname{Dot} 1(x, y) \\
{[p, s]=\operatorname{TwoProduct}\left(x_{0}, y_{0}\right)} \\
\text { for } i=1: n \\
{[h, r]=\operatorname{TwoProduct}\left(x_{i}, y_{i}\right)} \\
{[p, q]=\operatorname{TwoSum}(p, h)} \\
s=\mathrm{fl}(s+(q+r)) \\
\text { res }=\mathrm{fl}(p+s)
\end{gathered}
$$

ALGORITHM(6): Dot product in doubled working precision.
Proposition 2. Let $x_{i}, y_{i} \in \mathbb{F}, 0 \leq i \leq n$, be given and denote by res $\in \mathbb{F}$ the result computed by Algorithm 6 (Dot1). Then, if no underflow occurs,

$$
\left|r e s-x^{T} y\right| \leq e p s\left|x^{T} y\right|+\gamma_{n}^{2}\left|x^{T}\right||y|
$$

and in the presence of underflow,

$$
\mid \text { res }-x^{T} y|\leq e p s| x^{T} y\left|+\gamma_{n}^{2}\right| x^{T}| | y \mid+ \text { 5neta. }
$$

Algorithm 6 requires $25 n-7$ flops.

$$
\begin{gathered}
\text { function res }=\operatorname{Dot} K(x, y, K) \\
{\left[p, r_{0}\right]=\operatorname{TwoProduct}\left(x_{0}, y_{0}\right)} \\
\text { for } i=1: n \\
{\left[h_{i}, r_{i}\right]=\operatorname{TwoProduct}\left(x_{i}, y_{i}\right)} \\
{\left[p, r_{n+i-1}\right]=\operatorname{TwoSum}(p, h)} \\
r_{2 n}=p \\
\text { res }=\operatorname{SumK}(r, K-1)
\end{gathered}
$$

ALGORITHM(7): Dot product with K-fold summation, $K \geq 2$.
Proposition 3. Let $x_{i}, y_{i} \in \mathbb{F}, 0 \leq i \leq n$, be given and denote by res $\in \mathbb{F}$ the result computed by Algorithm 7 (DotK). Then, if no underflow occurs,

$$
\mid \text { res }-x^{T} y\left|\leq\left(\mathrm{eps}+2 \gamma_{4 n-1}^{2}\right)\right| x^{T} y\left|+\gamma_{4 n-2}^{K+1}\right| x^{T}| | y \mid,
$$

and in the presence of underflow,

$$
\mid \text { res }-x^{T} y\left|\leq\left(\mathrm{eps}+2 \gamma_{4 n-1}^{2}\right)\right| x^{T} y\left|+\gamma_{4 n-2}^{K+1}\right| x^{T}| | y \mid+5 n \text { eta. }
$$

Algorithm 7 requires $13 n+6 K(2 n-1)-1$ flops.
4. Numerical results. In this section we present computational results for the dot product in doubled working precision (Algorithm 6, Dot1) and for the dot product in $K$-fold working precision (Algorithm 7, DotK). This also tests the summation algorithms presented in Section 3. We will not say about the many practical applications of algorithms because $i$ ) this is widely known, and $i i$ ) there are excellent treatment in the recent literature [4], [8].

For the measurement of the actual computing time compared to BLAS, the algorithms were implemented in C++ and tested on the following high-Performance computing environment (ALiCE) [1]:

- Nodes: 128 Compaq DS10
- Processors: $\quad 616 \mathrm{MHz}$ Alpha 21264 EV6/7
- Cache: $\quad 2 \operatorname{Mbyte}\left(2^{n} d\right), 64 \mathrm{k} / 64 \mathrm{k}\left(1^{s} t\right)$
- Memory: $256 \mathrm{MB} /$ node $\rightarrow 32$ Gbytes
- Disks: $10 \mathrm{~GB} /$ node $\rightarrow 1.3 \mathrm{~TB}$
- Connectivity: $64 \mathrm{bit} / 33 \mathrm{MHz}$ Myrinet.
- Power: 15 kW

We used Compaq C ++ compiler and GNU $g++$ compiler, and BLAS routines were taken from Compaq Extended Math Library.

We tested Algorithm 6 (Dot1, corresponding to quadruple precision), and Algorithm 7 (DotK) for the dot product in K-fold working precision for dimension 100 to 5000 in steps of 100. This also tests summation algorithms.

A more practical application are programs or higher-level operations involving dot products. A typical such operation is matrix-vector multiplication. Therefore we wrote a rather straightforward code for the computation of $A * y-b$ for a given $n \times n$ matrix A and $n$-vectors $y$ and $b$ using Dot1 and tested against the corresponding BLAS routine DGEMV. For dimensions $100,200, \ldots, 3000$. We display the ratio of the measured computing time of this routine compared to the measured computing time of the BLAS routine DGEMV.

Example: Suppose

$$
X=\left|\begin{array}{c}
1 \\
w \\
-w \\
w \\
-w \\
\cdots \\
\cdots \\
w \\
-w \\
n
\end{array}\right|, Y=\left|\begin{array}{c}
1 \\
1 \\
1 \\
1 \\
1 \\
\cdots \\
\cdots \\
\cdots \\
\cdots \\
1
\end{array}\right|,\left|\begin{array}{ccccccc}
1 & w & -w & \cdots & \cdots & \cdots & n \\
2 & w & -w & \cdots & \cdots & \cdots & n-1 \\
\cdots & \ldots & \ldots & \ldots & \ldots & \ldots & \ldots \\
\cdots \ldots \ldots & \ldots & \ldots & \ldots & \ldots & \ldots & \cdots \\
n & w & -w & \cdots & \cdots & \cdots & 1
\end{array}\right|
$$

where $w=e^{20}$ and $n$ is even. We obtained the exact result by using Dot1 and DotK algorithms.

| minimum | median | maximum |
| :--- | :---: | :---: |
| 9.654 | 7.98204 | 19.5484 |

Table 1. Measured ratio of computing time for matrix-vector residual

| K | minimum | median | maximum |
| :---: | :---: | :---: | :---: |
| 1 | 0.33 | 0.5 | 1 |
| 2 | 16 | 22.43 | 38 |
| 3 | 23 | 38.33 | 47.15 |
| 4 | 29 | 45.2 | 54.81 |
| 5 | 34 | 47 | 69.36 |
| 6 | 39.5 | 59.2 | 79.36 |
| 7 | 44 | 67.5 | 87.94 |

Table 2. Measured ratio of computing time for dot products
5. Conclusion. The algorithms use only basic floating point operations addition, subtraction and multiplication, and they use only the same working precision as the data are given in. This offers the possibility to put them into numerical library algorithms since no special computer architecture is required. We stress again that the algorithms are based on the error-free transformations TwoSum and twoProduct. If those would be available directly from the processor, precise dot product evaluation in double working precision by Dot1 would cost only twice as much as the ordinary dot product.

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# ТОЧНО СКАЛАРНО ПРОИЗВЕДЕНИЕ ЗА ВИСОКО-ПРОИЗВОДИТЕЛНИ ИЗЧИСЛЕНИЯ 


#### Abstract

Хасан Ел-Оуни Представени са алгоритми за сумиране и скаларно произведение на числа с плаваща точка, които са бързи в термините на машинно време. Алгоритмите са широко приложими защото изискват само събиране, изваждани е умножение на числа с плаваща точка във формата на входните данни. Демонстрира се, че получените резултати имат същата точност, както изчисления в двоен или четворен формат.


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    Key words: High-performance computing, exact scalar product, fast algorithms, verified error bounds.

